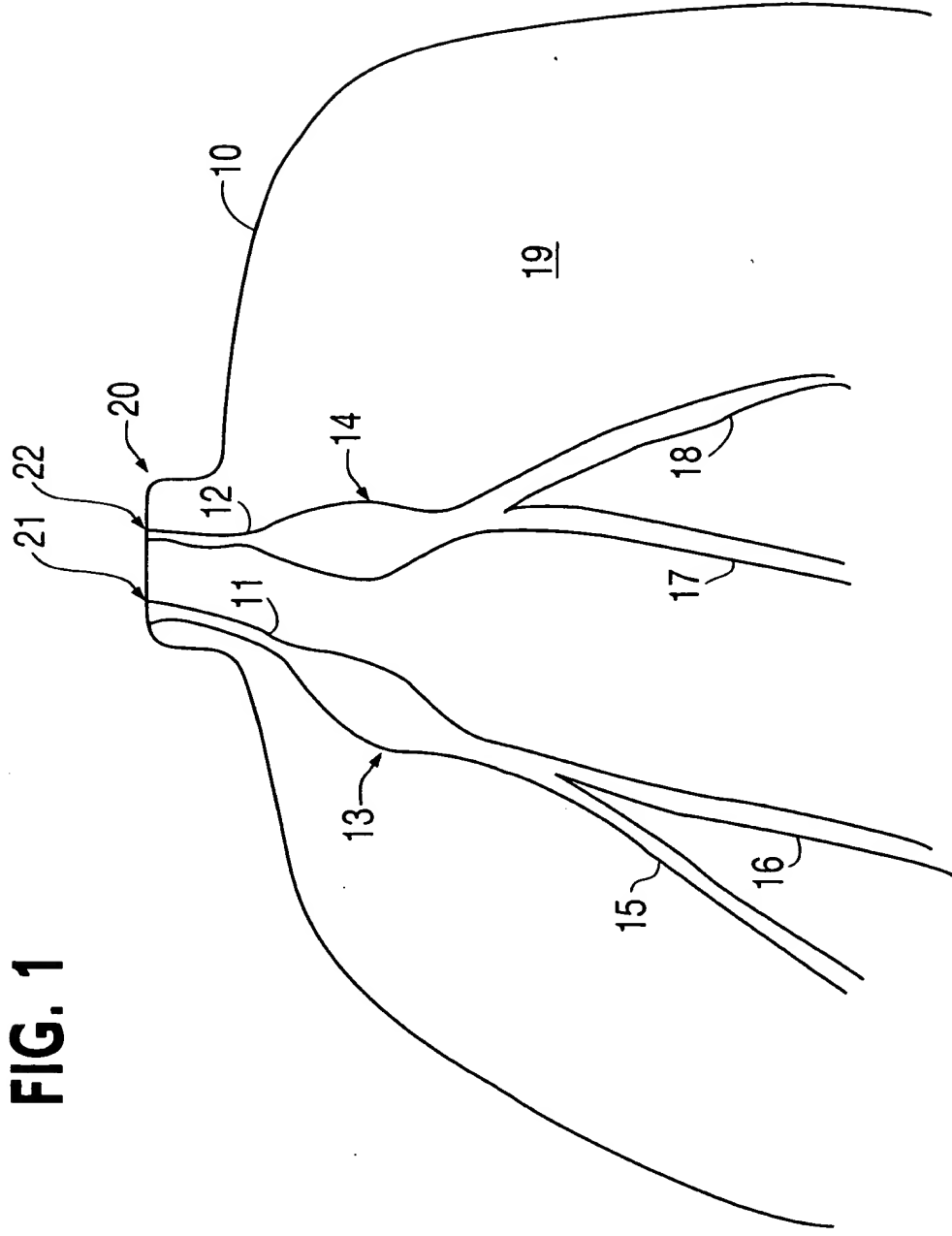
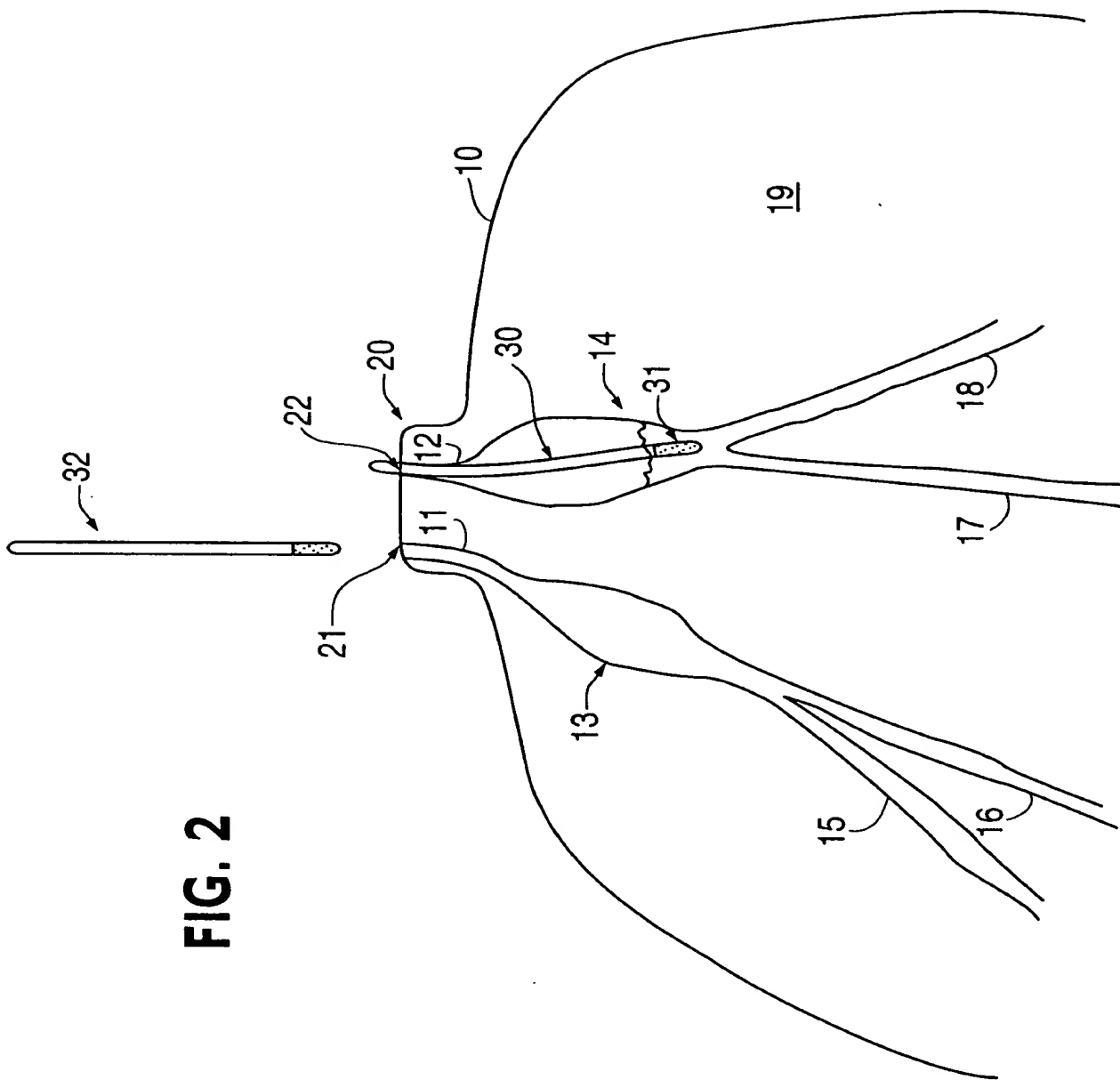


FIG. 1 is a schematic diagram of a device 10.

FIG. 1





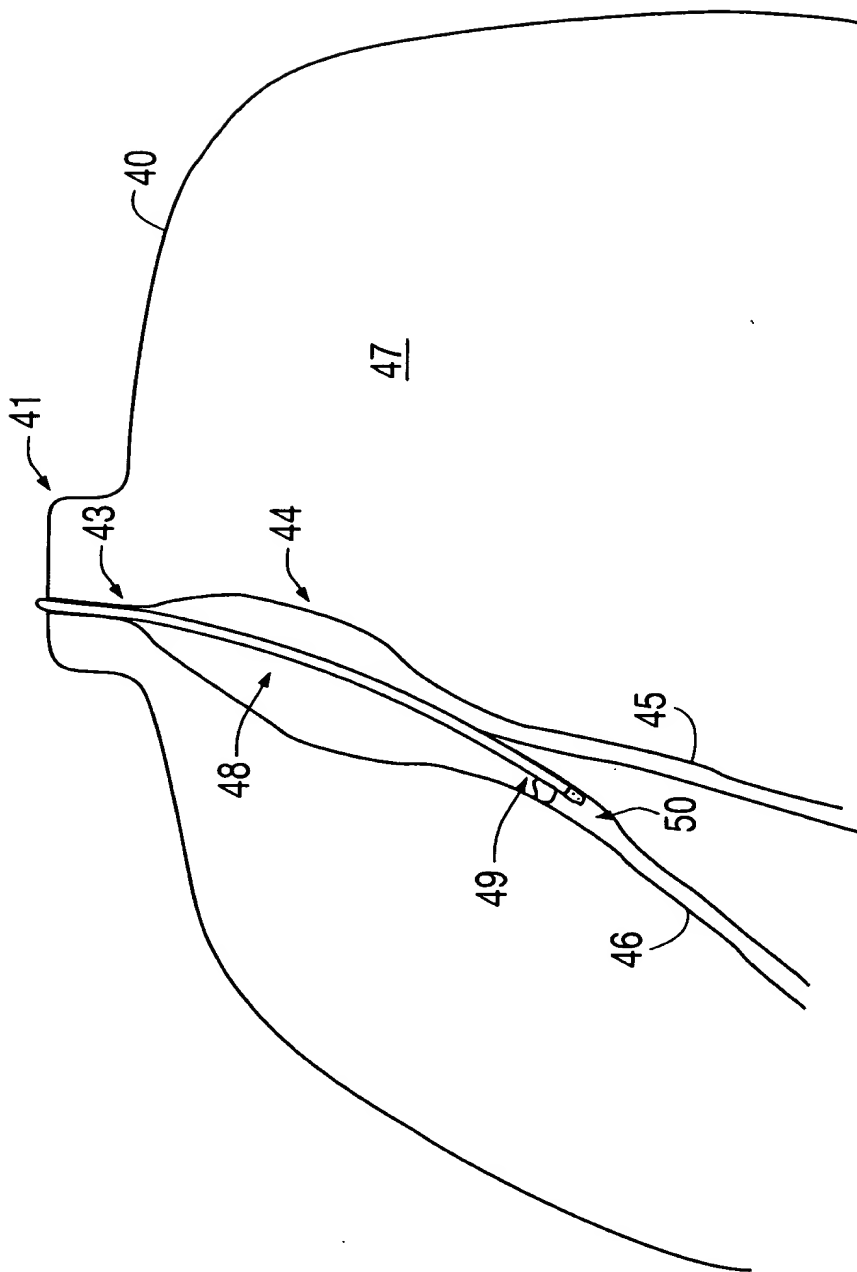


FIG. 4

